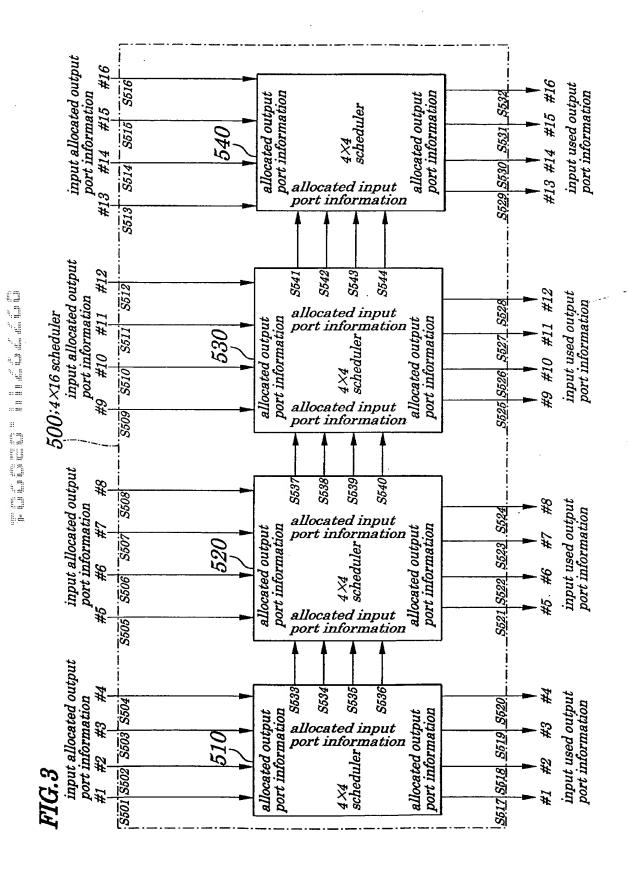
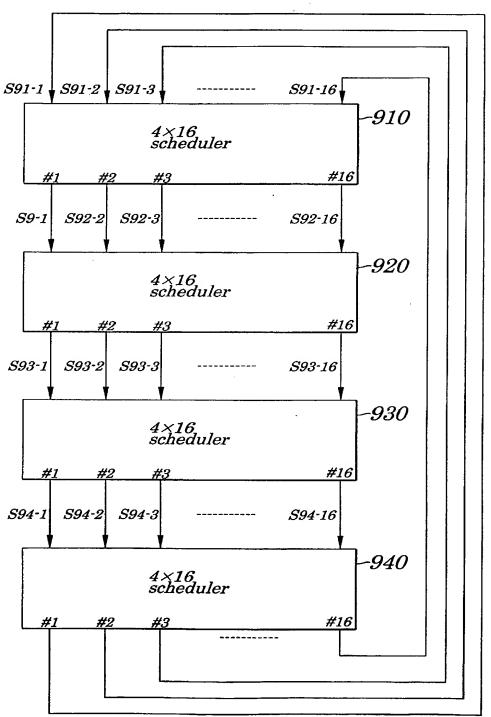


a:

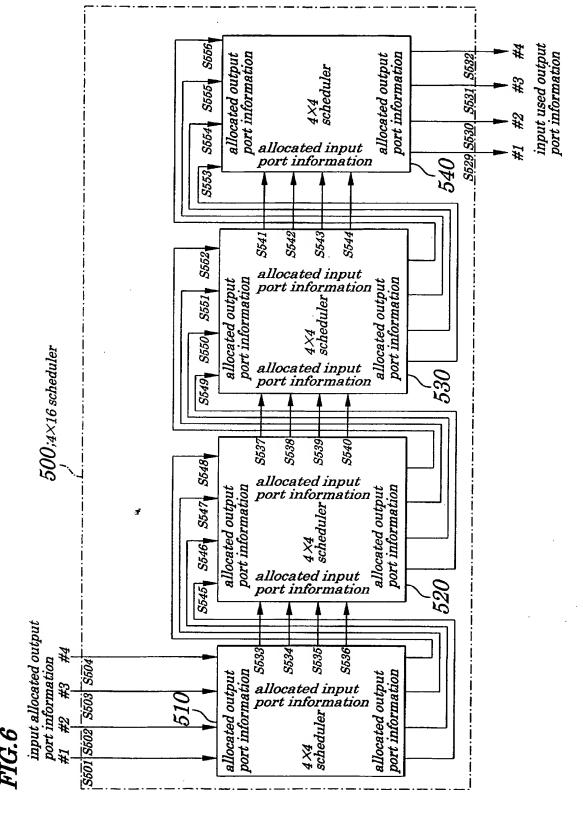


And the term that the term to the term that the term that

## allocated output port information



	910		026		930		940		TS12
	040		910		920		930		TS11
	930		940		910		920		ISIO   USII
TS16	920	TS15	930	TS14	940	TS13	910		GSL
	016		026		930		940		TS8
	940		016		026		930		LSL
	930		940		016		920		JSZ
TS12	920	TSII	930	OISL	940	6SL	016		TS5
	016		026		930		940		TS4
	040		016		026		930		TS3
	930		040		016		920		TSZ
TS8	920	LST	930	JSE TSE	940	TS5	016		TSI
	TS12 $TS16$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	930       940       910       920       930       940       910       920       930       940         TS11       TS11       TS15       TS15       TS15       TS15	930       940       910       920       930       940       910       920       930       940         940       910       920       940       910       920       940       910	930       940       910       920       930       940       910       920       930       940         940       910       920       930       940       910       920       940       910         940       910       920       930       940       910       920       940       910         TS10       TS10       TS14       TS14       TS14       TS14       TS14	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	930       940       910       920       930       940       910       920       930       940         940       910       920       930       940       910       920       930       940       910         940       910       920       930       940       910       920       930       940       910         910       920       930       940       910       920       930       940       910       920         77510       77513       775	930       940       910       920       930       940       910       930       940       910       930       940       910       930       940       910       930       940       910       930       940       910       930       940       910       930       9	930       940       910       920       930       940       910       920       930       940         940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       910       920       930       940       930       940       930       940       930       940       930       940       930       940       930       940       930       940       930       940       930       940       930       940       930       940       9



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FIG. 7

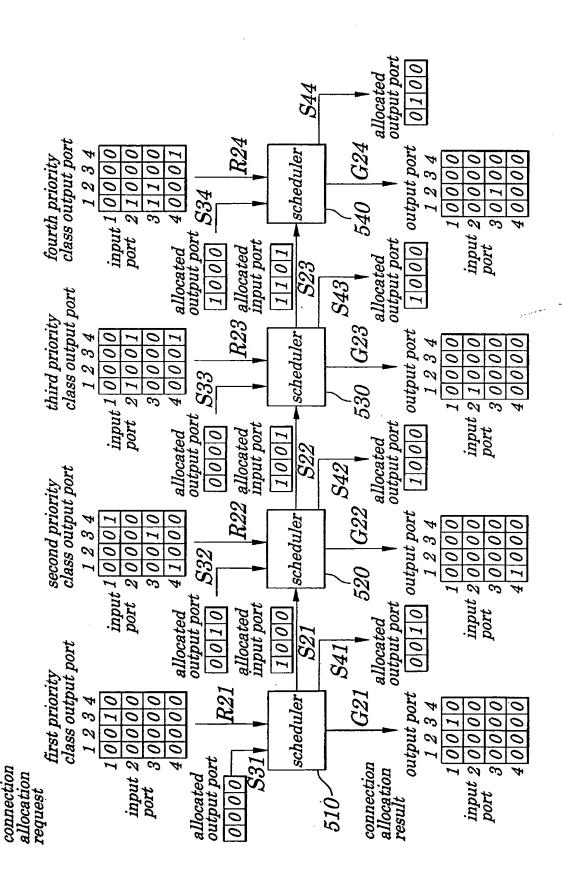
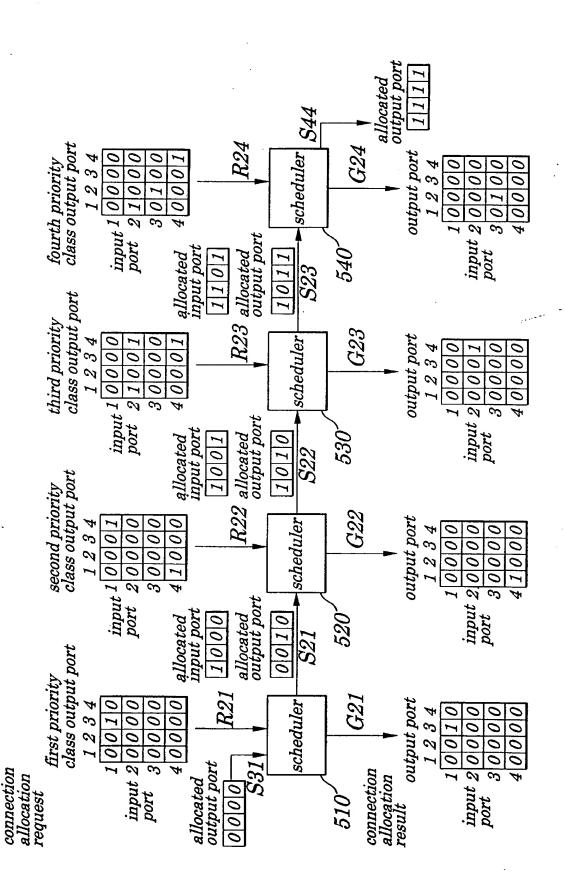


FIG.8



₽4.

FIG.9

allocated output port information

